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FILING DATE 5649-1149 10/644,462 08/20/2003 Jin-Sung Chung TITLE OF INVENTION: METHODS FOR FORMING METAL INTERCONNECTIONS FOR SEMICONDUCTOR DEVICES USING A BUFFER LAYER ON A TRENCH SIDEWALL, AND SEMICONDUCTOR DEVICES SO FORMED **PUBLICATION FEE** TOTAL FEE(S) DUE DATE DUE SMALL ENTITY ISSUE FEE APPLN. TYPE \$300 \$1630 08/16/2004 \$1330 NO nonprovisional **CLASS-SUBCLASS** ART UNIT **EXAMINER** 2812 438-597000 **GURLEY, LYNNE ANN** 1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. 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